

**GUJARAT TECHNOLOGICAL UNIVERSITY****BE - SEMESTER– III (New) EXAMINATION – WINTER 2019****Subject Code: 3130305****Date: 30/11/2019****Subject Name: Advanced Electronics****Time: 02:30 PM TO 05:00 PM****Total Marks: 70****Instructions:**

1. Attempt all questions.
2. Make suitable assumptions wherever necessary.
3. Figures to the right indicate full marks.

|            |   | <b>MARKS</b> |
|------------|---|--------------|
| <b>Q.1</b> | (a) Enlist characteristics of Op-Amp.   | <b>03</b>    |
|            | (b) Explain Op-Amp 741 package Style & pin diagram.   | <b>04</b>    |
|            | (c) Design amplifier circuits with following gain and draw output wave form for input $V_{in} = +1$ volt  | <b>07</b>    |
|            | (1) -5      (2) 2   |              |
| <b>Q.2</b> | (a) Draw & Explain open loop inverting configuration of op-amp.   | <b>03</b>    |
|            | (b) Draw and explain V to I converter circuit.  | <b>04</b>    |
|            | (c) Design a circuit which can generate output voltage $V_0 = -2V_1 + \frac{3}{2}V_2$                     | <b>07</b>    |
|            | Where $V_1$ and $V_2$ are the input voltage.  |              |
| <b>OR</b>  |   |              |
| <b>Q.3</b> | (c) Explain Basic Operation of SCR with necessary diagram.  | <b>07</b>    |
|            | (a) Give colors of Op-Amp noise with its frequency content.   | <b>03</b>    |
|            | (b) Draw the circuit of the differentiator and its frequency response.                                    | <b>04</b>    |
|            | (c) Draw and explain integrator circuit. What is the problem of integrator circuit? How it can be solved? | <b>07</b>    |
| <b>OR</b>  |   |              |
| <b>Q.3</b> | (a) Draw the circuit of Instrumentation amplifier.  | <b>03</b>    |
|            | (b) Justify “Transistor as a switch”.   | <b>04</b>    |
|            | (c) Explain common base configuration of transistor with necessary equations.                             | <b>07</b>    |
| <b>Q.4</b> | (a) Enlist type of Op-Amp noise. Explain any one type.  | <b>03</b>    |
|            | (b) Draw a circuit of wide band pass filter and its frequency response.                                   | <b>04</b>    |
|            | (c) Design a low-pass filter at a cutoff frequency of 1 KHz with passband gain of 1                       | <b>07</b>    |
| <b>OR</b>  |   |              |
| <b>Q.4</b> | (a) Explain positive fixed voltage regulator IC.  | <b>03</b>    |
|            | (b) Draw a circuit of wien bridge oscillator.   | <b>04</b>    |
|            | (c) Explain 555 times as a monostable multivibrator.  | <b>07</b>    |
| <b>Q.5</b> | (a) Explain basic operation of DIAC and TRIAC.  | <b>03</b>    |
|            | (b) Explain the basic structure, operation and breakover characteristics of schokley diode.               | <b>04</b>    |
|            | (c) Explain operation of Class A amplifier.   | <b>07</b>    |
| <b>OR</b>  |   |              |
| <b>Q.5</b> | (a) Explain Passive RC high pass filter.  | <b>03</b>    |
|            | (b) Explain the working of Electromagnetic relay with schematics.   | <b>04</b>    |
|            | (c) Design a phase shift oscillator for frequency 2KHz.   | <b>07</b>    |

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